


NOTE:  
FOR SCHEMATIC DIAGRAM AND CIRCUIT BOARD LAYOUT NOTES,  
REFER TO BEGINNING OF SCHEMATIC SECTION.

**CAUTION: FOR CONTINUED PROTECTION AGAINST FIRE HAZARD,  
REPLACE ONLY WITH THE SAME TYPE 2A 250V FUSE.**  
**ATTENTION: POUR UNE PROTECTION CONTINUE LES RISQUES  
D'INCENDIE N'UTILISER QUE DES FUSIBLE DE MÊME  
TYPE 2A 250V**

**IMPORTANT SAFETY NOTICE:**  
COMPONENTS IDENTIFIED BY THE SIGN  HAVE SPECIAL CHARACTERISTICS IMPORTANT FOR SAFETY. WHEN REPLACING ANY OF THESE COMPONENTS, USE ONLY THE SPECIFIED PARTS.

The block diagram illustrates the control logic for the power supply. It features a central vertical bus with terminals labeled 3 and 4. The logic components and their connections are as follows:

- LATCH**: Receives a signal from terminal 3 and outputs to **OVER CURRENT PROTECT**.
- START CONTROL**: Receives a signal from terminal 3 and outputs to an **AND** gate.
- OVER VOLTAGE PROTECT**: Receives a signal from terminal 3 and outputs to the same **AND** gate.
- THERMAL SHUTDOWN**: Receives a signal from terminal 3 and outputs to the same **AND** gate.
- AND Gate**: Receives inputs from **START CONTROL**, **OVER VOLTAGE PROTECT**, and **THERMAL SHUTDOWN**. Its output goes to **LATCH**.
- OVER CURRENT PROTECT**: Receives a signal from **LATCH** and outputs to an **OR** gate.
- OR Gate**: Receives inputs from **OVER CURRENT PROTECT** and **OSC**. Its output goes to **DRIVE**.
- OSC**: Receives a signal from terminal 4 and outputs to the **OR** gate.
- DRIVE**: Receives a signal from the **OR** gate and outputs to **SWITCHING**.
- SWITCHING**: Receives a signal from **DRIVE** and outputs to terminal 4.

**VJBS1026**